Name of the Faculty Vijay Bharat Desle Designation **Assistant Professor** vijaydesle.ee@bitseducampus.ac.in Email id 9624109040 contact number **Total Experience** Teaching (years, months) 4 years 06 months 01 years 00 months Industry (years, months) Working as an Assistant Professor at Electrical engineering Department of BIT since **Professional Summary** June2015. -Worked at KJIT, Savli as a Assistant Professor for 10months. - Worked as a Testing and commissioning engineer in Laxmi Associates, Makarpura and VMS services, Mumbai for 1 Year. Assistant Professor, Electrical Engineering Department-BIT, **Current Activities** Department student attendance coordinator. Electrical Power System, High Voltage Engineering **Specialization Areas** Elements of Electrical Design Subject Taught (UG/PG) Utilization of Electrical Energy and Traction High voltage engineering Electrical measurement and measuring instruments Economics of energy generation and supply (PG) **Qualifications** M.Tech (Electrical Power system) (2014) (CGPA- 7.69) Charotar University of Science & Technology, Changa, Anand B. Tech (Electrical Engineering) (2011) (with Honours) Rajasthan Technical University, Kota Diploma (Electrical Engineering) (2008) (Distinction) Polytechnic, M.S. University-Vadodara Well versed in simulation softwares like MATLAB, PSIM, MultiSim, **Skills** POWERWORLD Simulator, Word processing softwares like LaTex and MS office. NCC candidate (B - Certificate) **Achievements Research Project** -Nil-**Publications** 1. "ORPD in highly stressed system-A comparative study using DE and BAT

Activities and honours

Grant Received

-Nil--Nil-

algorithm", published in International Journal of Engineering Development and Research (IJEDR), ISSN:2321-9939, Volume 5, issue 2 June 2017.
"ORPD in highly stressed system-A comparative study with DE and BAT algorithm", published in International Journal for Scientific Research and Development (IJSRD), ISSN:2321-0613, Volume 5, issue 5 July 2017.